

Listing of Claims:

This listing of claims replaces all prior versions, and listings of claims in the application.

IN THE CLAIMS

1. (Currently Amended) A computer readable medium having stored thereon a data structure defining a physical block (~~hereafter~~ pblock) in a hierarchy ~~hierarchy~~ of pblocks which defines the same integrated circuit structure by reference to data in a netlist which defines a logical hierarchy ~~hierarchy~~, the data defining said pblock comprising:

A) a set of field or list containing pointers to data defining boundary pins ~~of or said pblock or containing data which defines boundary pins assigned to said pblock~~, said boundary pins for connecting to nets external ~~nets to the pblock and for connecting to nets internal to the pblock~~;

B) a field containing a pointer to a first parent physical block cellview (pcellview) which contains said pblock;

C) a field containing a pointer to a second pcellview data structure owned by said pblock, said second pcellview containing ~~data or~~ pointers to data which define lists of pins internal to the pblock, nets that connect to the internal pins and boundary pins of the pblock, child pblocks and child instances which have been assigned to said pblock and which define the functionality of said pblock; and

D) a field containing coordinates on a floorplan representing a surface of an integrated circuit on which ~~said~~ circuits assigned to said pblocks are to be formed, said coordinates being such as to define a geometric shape representing said pblock and the size thereof, said geometric shape being displayed on a computer display of a computer executing a floor planning process.

2. (Currently amended) The computer readable medium of claim 1 wherein the data structure further includes data that ~~said field or list containing pointers to data which defines boundary pins or containing data which defines boundary pins also contains pointers to data which define internal pins of instances contained within said pblock or contains data which defines said internal pins of instances contained within said pblock.~~

3. (Currently Amended) The computer readable medium of claim 1 wherein said data structure includes a pcellview data structure comprising:

E) a list containing data which defines a list of pblocks which are included or nested within said pblock defined by data elements A through D;

F) a list containing data or pointers to data on said netlist which define instances which have been assigned to said pblock;

G) a list containing data or pointers to data which define boundary pins of said pblock which connect to internal nets of said pcellview;

H) wherein the [[a]] list containing data or pointers to data which define internal physical nets which connect from boundary pins of said pblock to internal pins so as to complete the original connectivity between instances defined in said netlist; and

I) a field which contains a pointer to a data object representing a parent pblock that envelopes the pcellview data object defined by data elements E through I.

4. (Currently Amended) A computer readable medium having stored thereon a data structure defining a physical block cellview (~~hereafter~~ pcellview) owned by a particular pblock in a hierarchy of pblocks which defines the internals of said pblock which owns said pcellview, the data defining said pcellview comprising:

A) a list containing data which defines a list of pblocks which are included or nested within said pblock defined by data elements A through D;

B) a list containing data or pointers to data on ~~said a~~ netlist which define instances which have been assigned to said pblock;

C) a list containing ~~data or~~ pointers to data which define boundary pins of said pblock which connect to internal nets of said pcellview and to external nets of said pblock;

D) a list containing ~~data or~~ pointers to data which define internal physical nets which connect from boundary pins of said pblock to internal pins of said pblock so as to complete the original connectivity between instances defined in said netlist; and

E) a field which contains a pointer to a data object representing a parent pblock that envelopes the pcellview data object defined by data elements A through E.

5 - 7. (Cancelled)

8. (New) A processor-based method for representing a netlist that defines a logical hierarchy, comprising:

creating in a memory respective representations of a plurality of physical blocks (pblocks), wherein each pblock specifies,

a set of boundary pins of the pblock, the boundary pins for connecting to nets external to the pblock and for connecting to nets internal to the pblock;

a reference to a first parent physical block cellview (pcellview) that contains said pblock;

a reference to a second pcellview owned by the pblock, the second pcellview specifying lists of pins internal to the pblock, nets that connect to the internal pins and boundary pins of the pblock, child pblocks and child instances assigned to said pblock and that define the functionality of the pblock; and

a set of coordinates representing a surface of an integrated circuit on which each circuit assigned to the pblock is to be formed.

9. (New) The method of claim 8, further comprising:
- displaying instances of the netlist in a first pane on a display and the pblocks in a second pane on the display; and
  - highlighting display of instances of the netlist that are assigned to pblocks.